

IUCTE, BHU, Varanasi

The Inter University Centre for Teacher Education (IUCTE) is an autonomous institution, established by University Grants Commission (UGC) under section 12 (ccc), with a primary goal of fostering excellence in higher education. Dedicated to pedagogical and technological innovations, advisory services, and policy development, IUCTE strives to elevate the standards of teaching in India. Through global partnerships and networking, the center envisions cultivating top-tier educators who contribute to a sustainable ecosystem, thereby reshaping the landscape of higher education in the country. IUCTE plays a pivotal role in enhancing teaching quality, fostering global collaboration, and advocating for policy changes to propel advancements in the field of education within India.

VSIT, Mumbai

Vidyalankar School of Information Technology (VSIT), affiliated with the University of Mumbai and approved by AICTE, is a NAAC 'A' Grade accredited autonomous institution established in 2002. Offering undergraduate, postgraduate, and doctoral programmes across IT, Data Science, Management, Commerce, and Mass Media, VSIT combines modern infrastructure with experiential learning, research, industry collaboration, and holistic student development. Its focus is on preparing students for successful careers and meaningful contributions to society.

About the Program

Academic Excellence in the AI Era: Teaching, Research and Indian Knowledge Systems explores the intersection of Artificial Intelligence, Teaching, Research and Indian Knowledge Systems. The FDP focuses on Generative AI and Prompt Engineering, AI-enabled Teaching and Assessment, Digital Research Tools, Research Ethics and Academic Integrity, Learning Analytics, Data-Driven Education, Quality Enhancement, Future-Ready Curricula and Indian Knowledge Systems. Through expert-led sessions, workshops and case studies, participants will gain practical perspectives and tools to strengthen teaching, research productivity, academic data and integrate contemporary technology with India's rich knowledge traditions.

Objectives

- To equip faculty with practical AI, Generative AI and prompt engineering skills.
- To strengthen research through digital tools, academic databases and ethical practices.
- To use learning analytics to enhance teaching effectiveness and learner outcomes.
- To integrate Indian Knowledge Systems into teaching, research and curriculum.
- To foster academic excellence, innovation and multidisciplinary collaboration.

Sub-Themes

- Teaching Learning Process in the AI Era
- Research Excellence in the Digital Age
- Data-Driven Academic Excellence
- Indian Knowledge Systems and Contemporary Higher Education
- Quality Assurance and Future-Ready Curricula

Faculty Development Program on Academic Excellence in the AI Era: Teaching, Research and Indian Knowledge Systems

Organized by

**Vidyalankar School of Information
Technology, Mumbai**
in collaboration with
**Inter University Centre for Teacher
Education, BHU, Varanasi**



Dates

16 - 20 November, 2026



Registration

Registration Opens: 12 October, 2026

Registration Closes: 31 October, 2026

Registration Form:

SCAN



Note:

- No registration fee is required to attend the program.
- Accommodation and Fooding will be provided.
- The program is open to a limited number of participants (50 seats only).
- Selection will be based on a first-come, first-served basis.
- No TA/DA will be provided.
- Participants are required to bring their laptops during the program.
- Basic digital literacy is required.
- As per UGC guidelines, participation in this program will be considered for CAS.

Eligibility

Only serving faculty members working under University of Mumbai including those in Constituent, Affiliated, Government Managed, Self-financed and Minority institutions shall participate.

Patron

Prof. Prem Narayan Singh

Director, IUCTE, BHU,
Varanasi

Dr. Rohini Kelkar

Principal, VSIT,
Mumbai

Program Directors

Prof. Asheesh Srivastava

Dean (Academics &
Research), IUCTE

Dr. Sindhu Krishnan

Chief Operations Officer,
VSIT

Program Convenors

Dr. Raj Singh, IUCTE

Dr. Lakshmi Pillai, VSIT

Program Co-convenors

Dr. Raja Pathak, IUCTE

Dr. Sandip Khandekar, VSIT

Dr. Umesh Koyande, VSIT

Mr. Shrinath Shankar, VSIT



Faculty Development Program

Venue of the Program

IUCTE Campus, BLW Rd, Sundar Bagiya Colony, Naria, Varanasi, Uttar Pradesh 221005

Contact Details

Dr. Raj Singh

Mob.: 9044094937

Email: raj.singh@iucte.ac.in

Mr. Vikas Janu (Web Master)

Mob.: 9785575456

Mr. Shrinath Shankar

Mob.: 7977461526



VSIT

Vidyalankar School of
Information Technology

RE-ACCREDITED : GRADE 'A' BY NAAC

